

## Pinout Placa IO Nexys

```
NET "r0" LOC="T13" | IOSTANDARD = LVCMOS33;  
NET "r1" LOC="R13" | IOSTANDARD = LVCMOS33;  
NET "r2" LOC="T14" | IOSTANDARD = LVCMOS33;  
NET "r3" LOC="R12" | IOSTANDARD = LVCMOS33;  
NET "g0" LOC="P8" | IOSTANDARD = LVCMOS33;  
NET "g1" LOC="R11" | IOSTANDARD = LVCMOS33;  
NET "g2" LOC="T12" | IOSTANDARD = LVCMOS33;  
NET "g3" LOC="T10" | IOSTANDARD = LVCMOS33;  
NET "b0" LOC="A5" | IOSTANDARD = LVCMOS33;  
NET "b1" LOC="P9" | IOSTANDARD = LVCMOS33;  
NET "b2" LOC="D5" | IOSTANDARD = LVCMOS33;  
NET "b3" LOC="A7" | IOSTANDARD = LVCMOS33;  
NET "vsync" LOC="A9" | IOSTANDARD = LVCMOS33;  
NET "hsync" LOC="A12" | IOSTANDARD = LVCMOS33;  
NET "ps_data" LOC = "D12" | IOSTANDARD = LVCMOS33;  
NET "ps_clk" LOC = "C10" | IOSTANDARD = LVCMOS33;
```