

1. A. Petrescu, **Calculatoare numerice**, Note de curs, 1999-2000-2004, (site-ul: www.csit-sun.pub.ro).
2. A. Petrescu. I. Francisc, Z. Racovita, **Inițiere în Structura Calculatoarelor Electronice**, Editura Teora, 1996.
3. Barry Wilkinson, **Computer Architecture, Design and Performance**, Prentice Hall 1996
4. William Buchanan and Austin Wilson, **Advanced PC Architecture**, Addison-Wesley 2001
5. William Buchanan, **PC Interfacing, Communications and Windows Programming**, Addison-Wesley 2001
6. A. S. Tanenbaum, **Organizarea Structurată a Calculatoarelor**, Editura AGORA, 1999.
7. L.N. Vintan, **Arhitecturi de Procesoare cu Paralelism la Nivelul Instrucțiunilor**, Editura Academiei Romane. 2000.
8. D. Grigoras, **Calcul Paralel. De la sisteme la programarea aplicațiilor**, AGORA, 2000.
9. Z.F. Baruch, **Sisteme de intrare/ieșire ale calculatoarelor**, Editura Albastră, 2000
10. S.G. Shiva, **Computer Design and Architecture**, 3rd Ed. Marcel Dekker Publishing House, 2000.
11. J. Hennessy, D. Paterson, **Computer Architecture. A Quantitative Approach**, 2nd ed. 1996, Morgan Kaufmann Publishers, Inc.
12. J. Hennessy, D. Paterson, **Computer Organization and Design. The Hardware/Software Interface**, 2nd ed.. 1998, Morgan Kaufmann Publishers, Inc.
13. David A. Patterson & John L. Hennessy, **Organizarea si Proiectarea Calculatoarelor**, All Educational 2002
14. R. Baron, L. Higbie, **Computer Architecture**, 1992, Addison Wesley Publishing Company.
15. M. Morris Mano, Ch. R. Kime, **Logic and Computer Design Fundamentals**, 2nd ed. Prentice Hall International Inc., 1993.
16. Morris Mano, **Computer System Architecture**, Prentice Hall 2000
17. David Van den Bout, **The Practical XILINX[®] Designer Lab Book**, Version 1.5, 1999, Prentice Hall.
18. V.P. Heuring, H. F. Jordan, **Computer Systems Design and Architecture**, Addison Wesley Longman, Inc. 1997.
19. Etienne Sicard, Sonia Delmas Bendhia, **Deep-submicro CMOS circuit design Simulator in hands**, Version1.1, Brook Scole, 2003, <http://www.brookscole.com>.
20. W. Stallings, **Computer Organization and Architecture**, Prentice Hall, 2000.
21. D. Thomas, P. Moorby, **The Verilog Hardware Description Language**, Kluwer Academic Publishers, 1998
22. <http://6004.csail.mit.edu/6371>
23. <http://bwrc.eecs.berkeley.edu/classes/cs152>
24. <http://www.stanford.edu/class/ee108b/main.html>
25. Site-uri curs si Laborator CN I_2CB: <http://curs.cs.pub.ro>, <http://csit-sun.pub.ro>,
<http://csit-sun.pub.ro/~cpop>